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- damages,
- injunctive relief and
- other remedies of any kind

for past, current and future infringement; and

(g) all rights to collect royalties and other payments under or on account of any of the foregoing

12/3/2002	mes	Wight, James	Australia	2002351903	Ĺapsed	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001AU**
12/3/2001	ımes	Wight, James	China	2824126.6	Pending	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001CN
06/03/2004	imes	Wight, James	Korea	7008505/2004	Pending	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001KR
12/3/2002	mes	Wight, James	Japan	2003-550250	Pending	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001JP
12/3/2002	mes	Wight, James	PCT	CA02/01847	Nationalized	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001PC
12/3/2001	ines	Wight, Jaines	U.S.A.	6,603,352	Issued	Switched-Mode Power Amplifier Integrally Performing Power Combining	ICE-001
Filing Date	-14	Inventor	Country	Number	Status	Title	Item
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7/26/2002	mes	Wight, lames	EPO	2748525.9	Pending	Reception Diversity Combiner	ICE-002EP
7/26/2002	mes	Wight, James	Clúna	2818192.1	Pending	Reception Diversity Combiner with Selectable Inversion and Variable Gain	ICE-002CN
7/26/2002	mcs	Wight, James	Canada .	2455111	A.bandoned	Reception Diversity Combiner with Selectable Inversion and Variable Gain	ICE-002CA
7/26/2002	Ties	Wight, James	PCT	GA02/01150	Nationalized	Reception Diversity Combiner with Selectable Inversion and Variable Gain	ICE-002PC
2/6/2002	nes	Wight, James	u.s.n.	10/068,120	Abandoned	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	ICE-002
7/27/01	nes	Wight, James	U.S.A.	60/307/889	Expired	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	ICE-002PR
6/30/2003	nes	Wight, James	U.S.A.	6,937,096	Issued	Switched-Mode Power Amplifier Integrally Performing Power Combining (CIP)	ICE-001CP
Filing Date		Inventor	Country	Number	Status	<u>Title</u>	Item
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ICE-004	ICE-003AU**	ICE-003PC	ICE-003	ICE-002NO	ICE-002KR	ICE-002JP		Item	
Up/Down Conversion Circuitry for Radio Transceiver	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Selectable Inversion/Variable Gain Combiner for Diversity Reception in RF Transcrivers	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	with Selectable Inversion and Variable Gain	<u>Title</u>	
Pending	Lapsed	Expired	Allowed	Abandoned but revivable	Pending	Abandoned		Status	
10/154,282	2002328744	CA02/01498	10/094,826	20040269	7001206/2004	2003-518082		Number	·
U.S.A.	Australia	PCT	U.S.A.	Norway	Korea	Japan		Country	
Birkett, Alexander	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James		Inventor	
kander	mes	nes .	So .	nes	nes	es			
5/22/2002	10/4/2002	10/4/2002	3/11/2002	7/26/2002	01/27/2004	7/26/2002		Filing Date	

IC		ICE.	ICI	I(ICE-	ICI		
ICE-006JP	ICE-006	ICE-005AU**	ICE-005PC	ICE-005	ICE-004AU**	ICE-004PC	<u>Item</u>	
Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Frequency Offset Generator for Synthesized Signals	Frequency Offset Generator for Synthesized Signals	Oscillator Frequency Offsets	Up/Down Conversion Circuitry . for Radio Transceiver	Up/Down Conversion Circuitry for Radio Transceiver	<u>Title</u>	
Pending	Pending	Lapsed	Expired	Abandoned	i.apsed	Expired	Status	
2004-543858	10/273,908	2002328745	CA02/01499	10/155,107	2002328743	CA02/01497	Number	
Japan	U.S.A.	Australia	PCT	U.S.A.	Australia	PCT	Country	
Parker, Kevin	Parker, Kevin	Birkett, Alexander	Birkett, Alexander	Birkett, Alexander	Birkett, Alexander	Birkett, Alexander	Inventor	
evin	evin	rander	ander	ander	ander	ander		
04/15/2005	10/18/2002	10/4/2002	10/4/02	5/23/2002	10/4/2002	10/4/2002	Filing Date	·

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ICE-008JP	ICE-008CN	ICE-008KR	ICE-008	ICE-008PR	ICE-007PC	ICE-007	ICE-006PC	ICE-006AU**	Item	
Parallel Convolutional Encoder	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Systems and Modules for Use with Trellis-Based Decoding	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	<u>Title</u>					
Pending	Pending	Pending	Pending	. Expired	Expired	Pending	Expired	Lapsed	Status	
2004-525088	03818236.X	7001719/2005	10/629,644	60/399,728	CA04/000282	10/377,859	2004036862	2003278003	Number	
Japan	China	Korea	U.S.A.	U.S.A.	PCT	U.S.A.	PCT	Australia	Country	
Amer, Maher	Amer, Maher	Parker, Koyin	Parker, Keyin	Inventor						
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03/24/2005	07/31/2003	01/31/2005	7/29/2003	8/1/2002	2/26/04	2/28/2003	4/29/2004	10/14/2003	Filing Date	

										
ICE-010PR	ICE-009AU**		ICE-009PC	ונה מסט	ICE-009PR	ICE-008AU**	ICE-008PC	Item		
Processing Engines and Rli Circuitry for Multi-Carrier Modulation Transceivers	Parailel Scrambler/Descrambler	old unitable to the second of	Parallel Scrambler/Descrambler	r aidliei scrambler Descrambler	Darallal Cara-Li- T	Parallel Convolutional Encoder	Parallel Convolutional Encoder	Title		
Expired	Lapsed	Expired	Pending	Expired	гарзеа	ranonaricu	Mationalizad	Status		
60/277,941	2003249821	CA03/01132	10/629,640	60/411,343	2003249822	CA03/0113		Number		
U.S.A.	Australia	PCT	U.S.A.	U.S.A.	Australia	PCT	Conditi	Country		•
Wight, James	Amer, Maher	Amer, Maher	Amer, Maher	Amer, Maher	Amer, Maher	Amer, Naher	Johnaani	Invocate	 	
Sec.	ner .	her	her	her	her	her	17			
3/23/01	7/31/2003	7/31/2003	7/29/2003	9/18/02	7/31/2003	07/31/03	Filing Date			

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	ICE-010KR	ICE-010JP	ICE-010EP	ICE-010CN	ICE-010CA	ICE-010PC	ICE-010	Item	74
8 III A DIT CITITI PIC DOMINICO	Computational Circuits and Methods for Processing Modulated Signals Having	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Signal Decomposition for The Control Of its Dynamic Range	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Signal Decomposition for The Control Of its Dynamic Range	Signal Decomposition for The Control Of its Dynamic Range	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Title	
	Pending	Abandoned	Pending	Pending	Abandoned but Revivable	Nationalized	Abandoned	Status	
	7001445/2004	2003-518144	2748528.3	20818664.8	2,455,277	CA02/001174	09/918,106	Number	
	Korea	Japan	EPO	China	Canada	PCT	U.S.A.	Country	
	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Inventor	
	nes	nes	nes	nes	nes	nes	nes		
	01/30/2004	7/29/2002	7/29/2002	7/29/2002	7/29/2002	7/29/2002	7/30/2001	Filing Date	

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ICE-011EP	ICE-011PC	ICE-011JP	ICE-011	ICE-010CP	ICE-010NO		Item		
Chireix Architecture Using Low Impedance Amplifiers	Chireix Architecture Using Low Impedance Amplifiers	Chircix Architecture Using Low Impedance Amplifiers	Chireix Architecture Using Low Impedance Amplifiers	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Non-Constant Envelopes	Title		
Pending	Nationalized	Pending	Issued	Pending	Abandoned but Revivable		Status .		· ·
. 03769084	CA03/001546	2004-543859	6836183	10/205,743	20040367		Number		
EPO	PCT	Japan	U.S.A.	U.S.A.	Norway		Country	·	
Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	-	Tuventor		·
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10/14/2003	10/14/2003	04/15/2005	10/16/2002	7/26/2002	1/27/2004		Filing Date	·	

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	ICE-017 Integrat Wi ICE-018 Digita Adap ICE-019 Trans Phase	₽.		
	Integrated Circuit Incorporating Wire Bond Inductance Digital Branch Calibrator for An RF Transmitter Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP) Integrated Circuit Incorporating Wire Bond Inductance Digital Branch Calibrator for An RF Transmitter Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP) Integrated Circuit Incorporating Wire Bond Inductance Digital Branch Calibrator for An RF Transmitter Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Switched-Mode Power Amplifier Using Lumped lement Impedance Inverter for Parallel Combining Switched-Mode Power Amplifier Using Lumped lement Impedance Inverter for Parallel Combining (CIP) tegrated Circuit Incorporating Wire Bond Inductance Wire Bond Inductance Digital Branch Calibrator for An RF Transmitter Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments
Allowed .	Pending Pending	Pending Pending Pending	Issued Pending Pending	Status Issued Pending Pending
10/613,856	10/610,497	11/099,916	6,879,209 11/099,910 10/610,49	Number 6,879,209 11/099,916 10/610,49
<u> </u>	197 U.S.A. 381 U.S.A.			
	za	O FE	Gru Gru	
Saed, Aryan Saed, Aryan	ight, James	jh, Iohan James	h, Johan	h, Johan h, Johan James
7/25/2003	6/30/2003	4/6/2005	7/8/2003	Filing Date 7/8/2003 4/6/2005

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Inventor Filing Date Sacd, Aryan 8/13/2003 Saed, Aryan 8/13/2003 Saed, Aryan 8/13/2003 Saed, Aryan 8/13/2003 A. Birkett, Neil 9/12/2003 A. Amer, Maher 9/12/2003 A. Birkett, Neil 9/12/2003		-			
Saed, Aryan Saed, Aryan Saed, Aryan Saed, Aryan Birkett, Neil Birkett, Neil	U.S.A.	10/661,943	Pending	Method for Amplitude Insensitive Packet Detection	ICE-022
Saed, Aryan Saed, Aryan Saed, Aryan Birkett, Neil Birkett, Neil	U.S.A.	10/662,063	Pending	Optimized FFT/IFFT Module	ICE-021
Saed, Aryan Saed, Aryan Saed, Aryan Saed, Aryan Birkett, Neil	PCT	CA04/001566	Pending	Staggered AGC with Digitally Controlled VGA	ICE-020PC
Inventor Saed, Aryan Saed, Aryan Saed, Aryan	U.S.A.	10/661,945	Pending	Staggered AGC with Digitally Controlled VGA	ICE-020
Inventor Saed, Aryan Saed, Aryan	U.S.A.	10/641,373	Allowed	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	ICE-019CP3
<u>Inventor</u> Saed, Aryan	U.S.A.	10/641,374	Allowed	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	ICE-019CP2
Inventor	U.S.A.	10/641,371	Allowed	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	ICE-019CP1
Inventor				Phase and Delay Adjustments	
· ·	Country	Number	Status	<u>Title</u>	Item
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ICE-031	ICE-030	ICE-029PC	ICE-029	ICE-023PC	ICE-023	ICE-022PC	Item	
Multiple Input, Multiple Output Communications Systems	Systems and Methods for Rapid Signal Detection and Identification	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Frequency Domain Equalizer for Wireless Communications System	Frequency Domain Equalizer for Wireless Communications System	Method for Amplitude Insensitive Packet Detection	Title	
Pending	Pending	Pending	Pending	Pending	Pending	Pending	Status	
10/884,633	10/883,170	CA05/000153	10/779,322	CA04/001564	10/661,147	CA04/001565	Number	
U.S.A.	U.S.A.	PCT	U.S.A.	PCT	U.S.A.	PCT	Country	
Wight, James	Moher, Michael L	Wight, Jaines	Wight, James	Saed, Aryan	Saed, Aryan	Birkett, Neil	Inventor	
nes	ael L.	nes ·)es	ın	5			
07/02/2004	07/01/2004	2/7/2005	2/13/2004	8/26/04	9/12/2003	8/26/2004	Filing Date	

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(1) A authorizations re	Assignor respect to the ent	ICE-033	ICE-033PR	ICE-032	ICE-031C1	<u>Item</u>	
(1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required, to enter into this Agreement, make the assignments, and to carry out its obligations under this	Assignor represents, warrants and covenants (except that Purchaser makes no representation, warranty or covenant wi respect to the entries in the above chart that are Australian patent applications with Item designations ending in "AU**") that:	Power Amplifier	Improved Power Amplifier and Related Methods.	Power Amplifier	Multiple Input, Multiple Output Communications Systems (CIP)	<u>Title</u>	
ority, and has ol t, make the assig	(except that Pu tralian patent ap	Pending	Expired	Pending	Pending	Status	
otained all third pagnments, and to ca	rchaser makes no plications with Ite	Not yet assigned	60/325,301	10/884,627	10/954,429	Number	
arty consents, ap arry out its oblig	representation, em designations	U.S.A.	U.S.A.	U.S.A.	U.S.A.	Country	
provals and/or ations under the	warranty or covenant with ending in "AU**") that:	Grundlingh, J	Grundlingh, Johan	Parker, Keyin	Wight, James	Inventor	
other s	'enant' '*) th	Johan	ohan	'n.	es ·		
	with at:	11/07/2005	11/05/2004	7/02/2004	09/30/2004	Filing Date	

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of Assignee, its successors, gns and other legal representation the basis of satisfactory gcd to me that he/she execution, or the entity upon behalful.	WITNESS my hand and official seal. Signature Tuatta Mullingham Signature Tuatta Mullingham	COUNTY OF Middlesse.) SS. COUNTY OF Middlesse.) On Manhous James. before me, Mistricture (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.	ASSIGNOR By: Mune: Michael F. Schiavo Tille: Director (Signature MUST be notarized)	Exhibit I The terms and conditions of this Assignment of Patent Rights shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives. TH WITNESS WHEREOF this Assignment of Putent Rights is executed at
of the second of		Notary Public in and for said State, to me on the basis of satisfactory nowledged to me that he/she executes person, or the entity upon behalf of		Exhibit B of Assignee, its successors, ns and other legal representatives.

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